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| JC072 U.S. 09/09/00  | Class Issue Classification |
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U.S. UTILITY Patent Application

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| <p>O.I.P.E.</p> <p>AD SCANNED <i>IKB</i> Q.A. <i>TM</i></p> | <p>PATENT DATE</p> |
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|------------------------------|----------------------|----------------|-----------------|------------------------------------|-------------------------|
| APPLICATION NO. 09/898832 | CONT/PRIOR COP 12 | CLASS 21712 | SUBCLASS 205 | ART UNIT 2183 220 | EXAMINER Chan. Eddie |
|------------------------------|----------------------|----------------|-----------------|------------------------------------|-------------------------|

APPLICANTS

Thomas McDonald

234

ETI

Apparatus and method for selectively accessing disparate instruction buffer stages based on branch target address cache hit

2nd Construction stage wrap

PTO-2040
12/89

PTO-2040
12/89**ISSUING CLASSIFICATION**[illegible]

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|---|--|-------------|------------|-----------------------------------|----------------------|
| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ | _____ (Assistant Examiner) (Date) | | | NOTICE OF ALLOWANCE MAILED | |
| | _____ (Primary Examiner) (Date) | | | ISSUE FEE | |
| | | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | | ISSUE BATCH NUMBER | |

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